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L15

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<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>			
<u>L15</u>	L3 and L14	0	<u>L15</u>
<u>L14</u>	L13 and ((updat\$ or edit\$ or modif\$ or chang\$) near5 time\$ near5 valid\$)	53	<u>L14</u>
<u>L13</u>	L12 and (invalid\$ near5 time\$)	119	<u>L13</u>
<u>L12</u>	L6 and L9	349	<u>L12</u>
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<u>L10</u>	L3 and L9	13	<u>L10</u>
<u>L9</u>	L6 and (((delay\$ or wait\$ or freez\$ or disab\$) near5 time\$) same (pass\$ or over or gone))	349	<u>L9</u>
<u>L8</u>	L6 and (((delay\$ or wait\$ or freez\$ or disab\$) near5 time\$) same pass\$ or over or gone)	3292	<u>L8</u>
<u>L7</u>	L6 and ((delay\$ or wait\$ or freez\$ or disab\$) near5 time\$)	1411	<u>L7</u>
<u>L6</u>	(time\$ same invalid\$ same (updat\$ or modif\$ or edit\$ or chag\$))	4511	<u>L6</u>

<u>L5</u>	l2 not L4	55	<u>L5</u>
<u>L4</u>	l2 and L3	7	<u>L4</u>
<u>L3</u>	707/\$.ccls.	37654	<u>L3</u>
<u>L2</u>	L1 and ((delay\$ or wait\$ or freez\$ or disab\$) near5 time\$)	62	<u>L2</u>
<u>L1</u>	(time\$ and invalid\$ and (updat\$ or modif\$ or edit\$ or chag\$)).ab.	612	<u>L1</u>

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 Display Format: ☒ Citation ☐ Citation & Abstract

» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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- ☐ 1. **Distance-adaptive update protocols for scalable shared-memory multiproc**
 Raynaud, A.; Zheng Zhang; Torrellas, J.;
[High-Performance Computer Architecture, 1996. Proceedings. Second International Conference on](#)
 3-7 Feb. 1996 Page(s):323 - 334
 Digital Object Identifier 10.1109/HPCA.1996.501197
[AbstractPlus](#) | Full Text: [PDF](#)(1268 KB) IEEE CNF
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- ☐ 2. **Does multicast communication make sense in write invalidation traffic?**
 Hung-Chang Hsiao; Chung-Ta King;
[Parallel and Distributed Systems, 2000. Proceedings. Seventh International Conference on](#)
 4-7 July 2000 Page(s):221 - 228
 Digital Object Identifier 10.1109/ICPADS.2000.857702
[AbstractPlus](#) | Full Text: [PDF](#)(748 KB) IEEE CNF
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- ☐ 3. **Formal automatic verification of cache coherence in multiprocessors with memory models**
 Fong Pong; Dubois, M.;
[Parallel and Distributed Systems, IEEE Transactions on](#)
 Volume 11, Issue 9, Sept. 2000 Page(s):989 - 1006
 Digital Object Identifier 10.1109/71.879780
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Term:	12 not L4	 
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<u>L5</u>	12 not L4	55	<u>L5</u>
<u>L4</u>	12 and L3	7	<u>L4</u>
<u>L3</u>	707/\$.cls.	37654	<u>L3</u>
<u>L2</u>	L1 and ((delay\$ or wait\$ or freez\$ or disab\$) near5 time\$)	62	<u>L2</u>
<u>L1</u>	(time\$ and invalid\$ and (updat\$ or modif\$ or edit\$ or chag\$)).ab.	612	<u>L1</u>

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Term:	20040199813.pn.	▲	▼
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side by side			
	<i>DB=PGPB; PLUR=YES; OP=ADJ</i>		
<u>L4</u>	20040199813.pn.	1	<u>L4</u>
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>		
<u>L3</u>	L2 not l1	13	<u>L3</u>
<u>L2</u>	(invalidat\$ and time and (delay\$ or freez\$ or disab\$) and (updat\$ or modif\$ or edit\$ or chang\$)).ab.	38	<u>L2</u>
<u>L1</u>	(invalidat\$ and time and (delay\$ or freez\$) and (updat\$ or modif\$ or edit\$ or chang\$)).ab.	25	<u>L1</u>

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